Product data sheet

1. General description

High power density, hyperfast recovery rectifier with high-efficiency planar technology, encapsulated in a small and flat lead CFP5 (SOD128) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Reverse voltage V_R ≤ 200 V
- Forward current I_F ≤ 3 A
- Switching time t_{rr} ≤ 30 ns
- · Pt doped life time control
- Low inductance
- · Small and flat lead SMD plastic package
- Package height typ. 1 mm
- High power capability due to clip-bond technology
- · Planar die design
- · Capable for reflow and wave soldering
- AEC-Q101 qualified

3. Applications

- General-purpose rectification
- · Reverse polarity protection
- · Hyperfast switching
- Freewheeling applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} \leq 155 °C		-	-	3	Α
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	-	200	V
V _R	reverse voltage			-	-	200	V
V _F	forward voltage	I _F = 3 A; T _j = 25 °C	[1]	-	875	980	mV
		I _F = 3 A; T _j = 125 °C	[1]	-	730	820	mV
I _R	reverse current	V _R = 200 V; T _j = 25 °C	[1]	-	-	1	μΑ
		V _R = 200 V; T _j = 125 °C	[1]	-	1.5	35	μΑ

^[1] Very short pulse, in order to maintain a stable junction temperature.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		K [1] A
2	А	anode	1 2 CFP5 (SOD128)	006aab040

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PNE20030EP		plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128

7. Marking

Table 4. Marking codes

Type number	Marking code
PNE20030EP	DG

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 601134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	200	V
V _R	reverse voltage			-	200	V
V _{RMS}	RMS voltage			-	140	V
l _F	forward current	δ = 1; T _{sp} ≤ 150 °C		-	4.2	Α
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 155 °C		-	3	А
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; single half sine wave (applied at rated load condition); $T_{j(init)}$ = 25 °C		-	75	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.81	W
			[2]	-	1.3	W
T _j	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

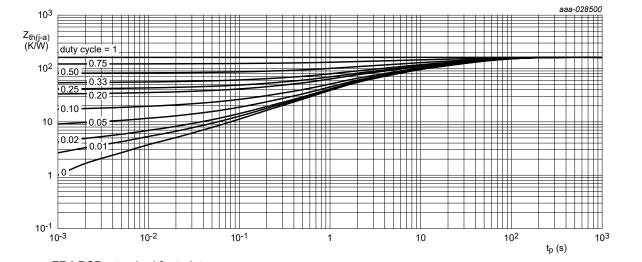
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9. Thermal characteristics

Table 6. Thermal characteristics

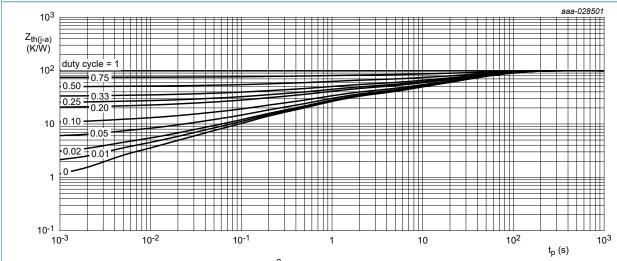
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	-	185	K/W
ju	junction to ambient		[2]	-	-	115	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[3]	-	-	8	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

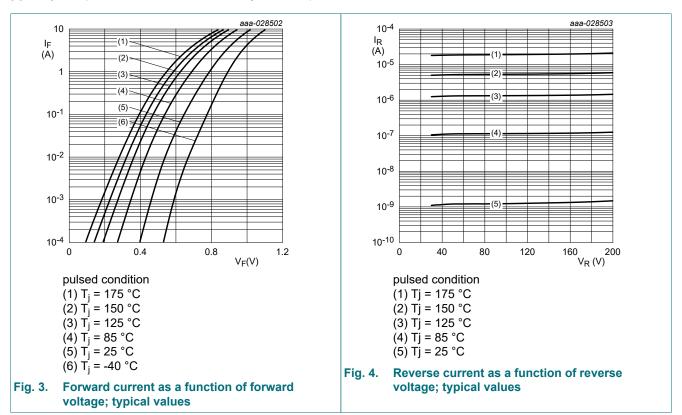
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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	I _R = 100 μA; T _j = 25 °C	[1]	200	-	-	V
V _F	forward voltage	I _F = 3 A; T _j = 25 °C	[1]	-	875	980	mV
		I _F = 3 A; T _j = 125 °C	[1]	-	730	820	mV
I _R	reverse current	V _R = 200 V; T _j = 25 °C	[1]	-	-	1	μΑ
		V _R = 200 V; T _j = 125 °C	[1]	-	1.5	35	μΑ
C _d	diode capacitance	V _R = 4 V; f = 1 MHz; T _j = 25 °C		-	32	-	pF
t _{rr}	reverse recovery time; step recovery	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$		-	13	30	ns
	reverse recovery time; ramp recovery	$I_F = 1 \text{ A}$; $dI_F/dt = 50 \text{ A/}\mu\text{s}$; $V_R = 30 \text{ V}$; $T_j = 25 \text{ °C}$		-	22	-	ns
		I _F = 1 A; dI _F /dt = 100 A/µs; V _R = 30 V;		-	17	-	ns
I _{RM}	peak reverse recovery current	T _j = 25 °C		-	1	-	Α
Q _{rr}	reverse recovery charge			-	16	-	nC
V_{FRM}	peak forward recovery voltage	$I_F = 1 \text{ A}; \text{ d}I_F/\text{d}t = 50 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$		-	820	-	mV

[1] Very short pulse, in order to maintain a stable junction temperature.



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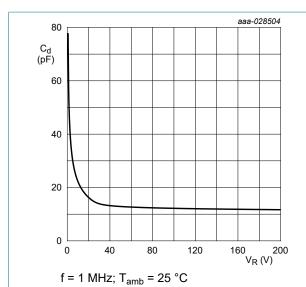
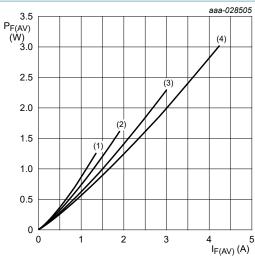
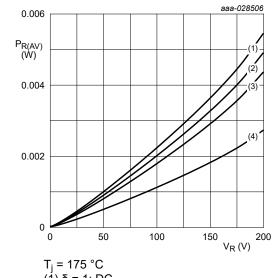


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



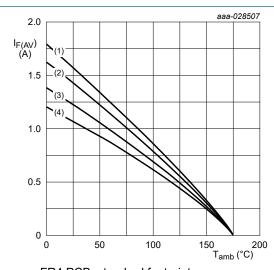
 $T_i = 175 \,{}^{\circ}\text{C}$ $(1) \delta = 0.1$ $(2) \delta = 0.2$ $(3) \delta = 0.5$ $(4) \delta = 1 (DC)$

Average forward power dissipation as a function of average forward current; typical values



 $(1) \delta = 1; DC$ $(2) \delta = 0.9$ $(3) \delta = 0.8$ $(4) \delta = 0.5$

Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T_i = 175 °C $(1) \delta = 1$; DC

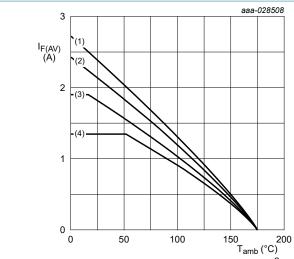
(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values

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FR4 PCB, mounting pad for cathode 1 cm²

T_i = 175 °C

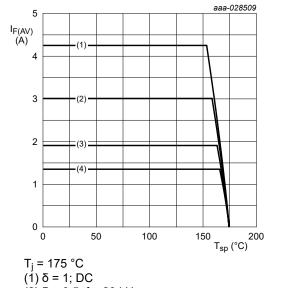
 $(1) \delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Average forward current as a function of Fig. 9. ambient temperature; typical values



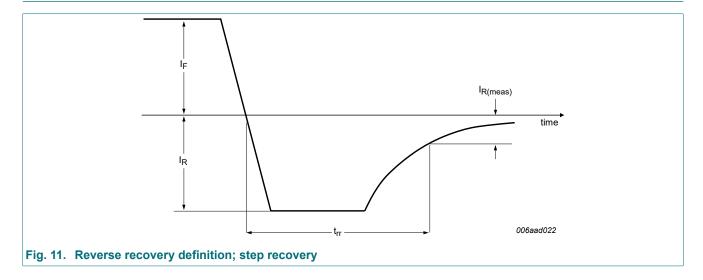
 $(2) \delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values

11. Test information



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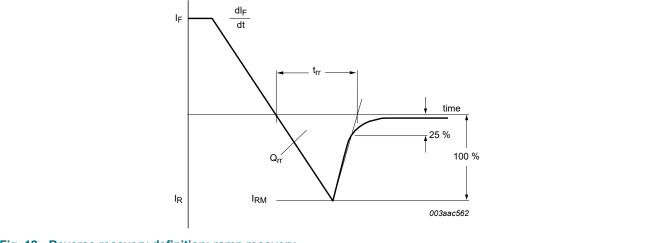


Fig. 12. Reverse recovery definition; ramp recovery

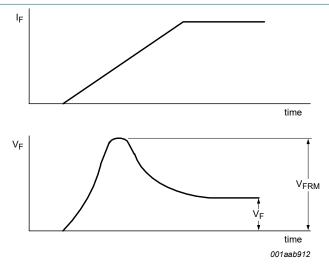


Fig. 13. Forward recovery definition

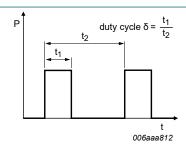


Fig. 14. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}\!\!=\!\!I_M\!\!\times\!\!\delta$ with I_M defined as peak current

 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$

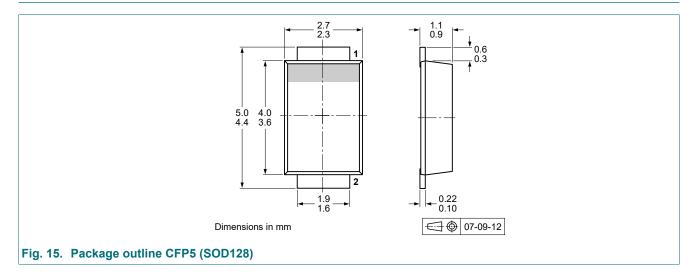
with $I_{\mbox{\scriptsize RMS}}$ defined as RMS current.

Quality information

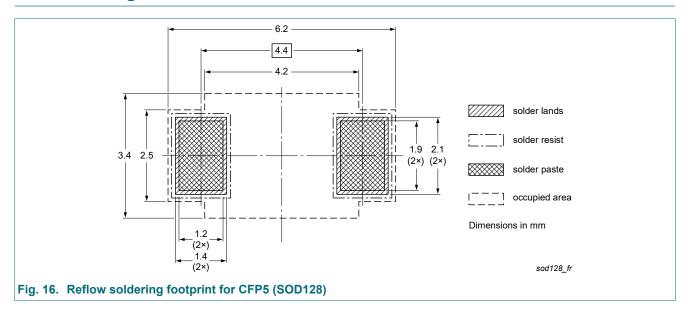
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline

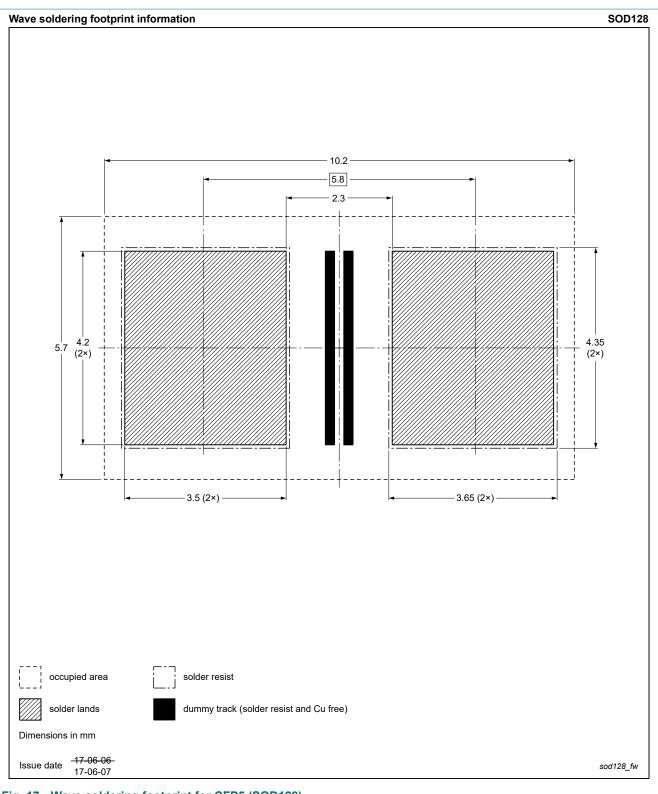


13. Soldering



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14. Revision history

Table 8. Revision history

Table 6. Reviolen metery							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PNE20030EP v.4	20191218	Product data sheet	-	PNE20030EP v.3			
Modifications:	Preliminary version of	of the AEC-Q101 qualified	d data sheet reached Pro	duct data sheet status			
PNE20030EP v.3	20191125	Preliminary data sheet	-	PNE20030EP v.2			
PNE20030EP v.2	20191120	Product data sheet	-	PNE20030EP v.1			
PNE20030EP v.1	20190227	Objective data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PNE20030EF

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